

PATENT
8001-1158

IN THE U.S. PATENT AND TRADEMARK OFFICE

Applicant: Rieka OHUCHI Conf.:

Appl. No.: NEW NON-PROVISIONAL Group:

Filed: March 11, 2003 Examiner:

For: SEMICONDUCTOR DEVICE AND METHOD OF
MANUFACTURING THEREOF

CLAIM TO PRIORITY

Assistant Commissioner for Patents
Washington, DC 20231

March 11, 2003

Sir:

Applicant(s) herewith claim(s) the benefit of the priority filing date of the following application(s) for the above-entitled U.S. application under the provisions of 35 U.S.C. § 119 and 37 C.F.R. § 1.55:

<u>Country</u>	<u>Application No.</u>	<u>Filed</u>
JAPAN	2002-066942	March 12, 2002

Certified copy(ies) of the above-noted application(s) is(are) attached hereto.

Respectfully submitted,

YOUNG & THOMPSON

Benoit Castel, Reg. No. 35,041

745 South 23rd Street
Arlington, VA 22202
Telephone (703) 521-2297

BC/baf

Attachment(s): 1 Certified Copy(ies)

COPY

日本国特許庁
JAPAN PATENT OFFICE

別紙添付の書類に記載されている事項は下記の出願書類に記載されて
いる事項と同一であることを証明する。

This is to certify that the annexed is a true copy of the following application as filed
with this Office

出願年月日

Date of Application:

2002年3月12日

出願番号

Application Number:

特願2002-066942

ST.10/C]:

[JP2002-066942]

出願人

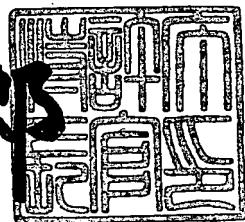
Applicant(s):

日本電気株式会社

2002年12月13日

特許庁長官
Commissioner,
Japan Patent Office

太田 信一郎



出証番号 出証特2002-3098926